

# Variable Photonic Time Delay System

up to ms long delay, ns fast delay change, 25-bit highest resolution



## Features

- Switching Option Available
- Smaller Size and Less Weight
- 0.1 - 40 GHz Bandwidth
- Flat Phase Response
- Minimal Triple-Transit Echoes
- Low Link Loss Options
- Low-Temperature Sensitivity

## Applications

- Radar System Testing
- Phased Array Antennas
- Signal Processing
- Electronic Warfare (EW) Systems

The Variable Photonic Time Delay system (SLTD or LLTD) is a turn-key unit that provides true-time delay with a fiber input and output. Perfected over 15 years, it uniquely features a long delay of up to ms, ultra-fast (ns) delay variation speed, and up to 25 bits high resolution. Internally, the input optical signal propagates through various fiber optic loops before exits. Delay time can be digitally varied by switching to pass through N fiber segments, therefore providing N-bit resolution. Delay length and system performance can be tailored to meet customer-specific requirements with great flexibility. The optical loss difference can be compensated by incorporating amplifications in long fiber segments. We offer three types of fiber optical switches: MEMS (ms), CL (ms), and NS (ns) to meet application requirements. USB/GUI or RS232 interface is a standard while high-speed switching is controlled by TTL through a D connector. Front Touch-screen computer is also a choice for system control.

The system delivers unmatched performance for radar testing, signal processing, phased array antennas, and phase noise testing with greater flexibility than traditional coax or waveguide solutions.

## Specifications

Parameter		Min	Typical	Max	Unit
Center Wavelength		1310		1550	nm
Wavelength Range			±50		nm
Delay Range		0.01		1000	μs
Delay Accuracy <sup>[1]</sup>		0.1	1		ns
Delay Increment		0.1			ns
Delay Resolution			20	25	Bit
System Intrinsic Delay <sup>[2]</sup>	MEMS		100	200	ns
Delay Changing Speed	CL		10	20	ms
	NS		50	100	μs
			0.1	0.3	μs
Cross Talk	MEMS	50	60	70	
	CL	40	45	50	dB
	NS	30	35	40	
Repetition Rate	MEMS			10	Hz
	CL			2	KHz
	NS			1	MHz
Insertion Loss <sup>[3]</sup>		0		TBD	dB
Insertion Loss Uniformity <sup>[4]</sup>			0.5	1	dB
Return Loss <sup>[4]</sup>		50			dB
Optical Power Handling			100	500	mW
Operating Temperature		0		60	°C
Storage Temperature		-40		85	°C
Power Supply		110		240	AVC
Power Consumption				250	W
Size			19" mount rack		

[1] It is defined @ maximum delay <=10us.

[2] Depending on the bit number N, such as 100ns up to 19-bit.

[3] The loss can be compensated by optical amplifiers

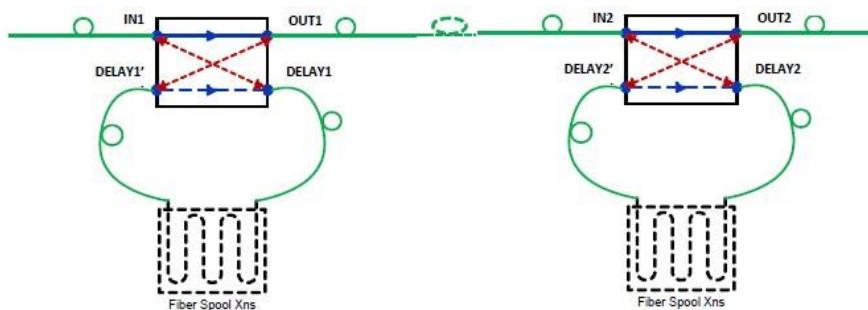
[4] Between delaying or bypassing at same bit delay loop

# Variable Photonic Time Delay System

up to ms long delay, ns fast delay change, 25-bit highest resolution

## Optical Path Diagram

Switchable fiber loops in series



The variable time delay module selectively routes optical signals through N fiber segments having different lengths. Each fiber segment is defined to have the delay as

$$\Delta T_i = 2^{(i-1)} \delta T, i=1, 2, \dots, N$$

Where  $\delta T$  is the increment of time delay. Therefore, the module provides N-bit of digitally variable time delay, having the total time delay as

$$\Delta T_{Total} = (2^N - 1)\delta T$$

N and  $\delta T$  are defined by the customer.

$$\text{Fiber Length} = \text{delay time} * \text{index of fiber} (\text{index of fiber } \sim 1.456)$$

## Enclosure Dimension (mm)

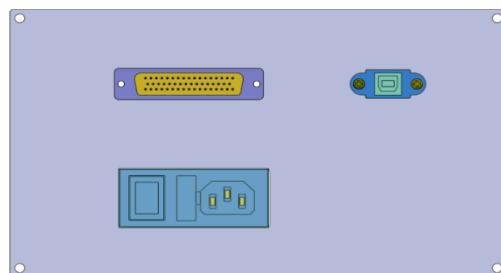
Typical 6U 19" rack for 16-19 bit system with the maximum delay of 0.1ms.

Front Panel (TBD)



- a. Width of panel: 482mm
- b. Width of rack: 420mm
- c. Max. deep of rack: 550mm

Rear Panel (TBD)



# Variable Photonic Time Delay System

up to ms long delay, ns fast delay change, 25-bit highest resolution

## Ordering Information

	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	0	<input type="checkbox"/>				
Prefix	Resolution	Wavelength	Switch Type	Package	Fiber Type <sup>[3]</sup>	Control <sup>[4]</sup>	Delay Range	Connector	
<b>SLTD- <sup>[1]</sup></b>	19bit = 19	1550nm = 5	CL/Latch = 2	3RU = 3	SMF-28 = 1	TTL = 1	Customized = 0	FC/PC = 2	
<b>LLTD- <sup>[2]</sup></b>	17bit = 17	1310nm = 3	NS/non-latch = 3	4RU = 4	DSP CMP fiber <sup>[5]</sup> = 2	USB = 2		FC/APC = 3	
	16bit = 16	Special = 0	MEMS/non-latch = 4	5RU = 5	Special = 0	RS232 = 3		SC/PC = 4	
	...		MEMS/latch = 5	6RU = 6		TouchScreen = 5		SC/APC = 5	
	9bit = A9		CL+MEMS = 6	7RU = 7		Special = 0		ST/PC = 6	
	8bit = A8		Special = 0	8RU = 8				LC/APC = 7	
	...			Special = 0				LC/UPC = U	
	1bit = A1							Special = 0	

[1]. Standard

[2]. Lossless

[3]. The dispersion compensated fiber must be used in the applications of long delay and high RF frequency. Please consult us on it.

[4]. Repeat rate of switching > 2kHz must use TTL control.

[5]. Dispersion compensation fiber

### Fiber Core Alignment

Note that the minimum attenuation for these devices depends on excellent core-to-core alignment when the connectors are mated. This is crucial for shorter wavelengths with smaller fiber core diameters that can increase the loss of many decibels above the specification if they are not perfectly aligned. Different vendors' connectors may not mate well with each other, especially for angled APC.

### Fiber Cleanliness

Fibers with smaller core diameters (<5 μm) must be kept extremely clean, contamination at fiber-fiber interfaces, combined with the high optical power density, can lead to significant optical damage. This type of damage usually requires re-polishing or replacement of the connector.

### Maximum Optical Input Power

Due to their small fiber core diameters for short wavelength and high photon energies, the damage thresholds for device is substantially reduced than the common 1550nm fiber. To avoid damage to the exposed fiber end faces and internal components, the optical input power should never exceed 20 mW for wavelengths shorter 650nm. We produce a special version to increase the how handling by expanding the core side at the fiber ends.